AGGREGATOR

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ZBAG-MA-RY21

DATA SHEET



PRODUCT DESCRIPTION

The aggregator is used in the process of collecting and aggregating useful data from multiple subsystems, controllers, devices etc. The main goal of data aggregation algorithms is to gather and aggregate data in an energy-efficient manner so that the network lifetime is enhanced. Aggregator supplements DDC's/ field controller's limited computational power and limited memory and enhances the performance/output with edge computing feature.

FEATURES

- Data Aggregation
- Uplink- Cloud
- Edge Computing



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MODULE SPECIFICATION

MODEL NUMBER

PROCESSOR

RAM

MEMORY

POWER SUPPLY

DIMENSIONS

INSTALLATION

OPERATING CONDITION

INTERFACE

ZBAG-MA-RY21

Quad-core cortex A53 processor at 1.2Ghz

1 GB DDR3 - RAM.

Up to 64GB externalSD CARD

30 Days of data storage with 10000 Data points per

day.

5 VDC/1 AMPS

132 X 92X 35mm (LXWXH)

Wall Mount (using 3mm Screw)

0°C to 50°C, 10-90 % RH, non-condensing, Degree of protection: IP40, IP20 (terminals)

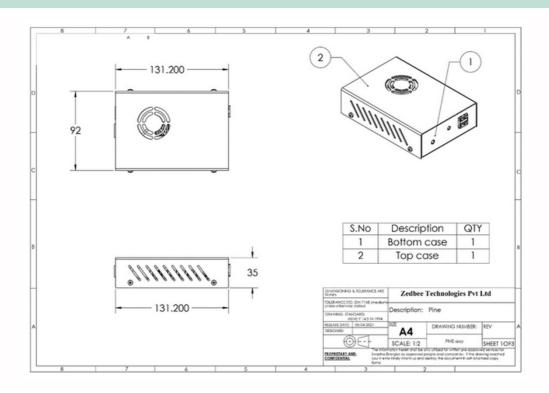
GSM (Optional) and Wi-Fi for uplink(Optional).

Internal RTC with Battery Backup(Optional).

Local Debug USB port 10/100 Ethernet Port

MODBUS TCP/IP Port for downlink.

DIMENSION



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